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AUG 26 2002
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OFGS File No.: IR-1677 (2-1984)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Zhijun Qu et al.

COPY OF PAPERS
ORIGINALLY FILED

Serial No.: 09/329,156

Date: August 21, 2002

Filed: June 9, 1999

Group Art Unit: 2811

For: DUAL EPITAXIAL LAYER FOR HIGH VOLTAGE VERTICAL
CONDUCTION POWER MOSFET DEVICES

AF/281
#20
Notice
of Appeal
9-502
arr
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Asst. Commissioner of Patents and Trademarks
Washington, DC 20231

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision dated May 21, 2002 of the Primary Examiner finally rejecting claims 1-4 and 9.

The Appeal Fee (\$320 - Non-Small Entity) is

☒ Enclosed

☐ Not required (fee paid in prior appeal)

☐ Charge to Deposit Account No. 15-0700

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. §1.136(a), to extend the time for filing a response by the number of months which will avoid abandonment under 37 C.F.R. §1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 15-0700. In the event that the actual Appeal Fee is greater than the payment submitted herewith or is not enclosed, the Patent Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Asst. Commissioner of Patents and Trademarks, Washington, D.C. 20231, on August 21, 2002

Respectfully submitted,

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Name of applicant, assignee or
Registered Representative

Brendan J. Kennedy
Signature

August 21, 2002

Date of Signature